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SERIAL NUMBER 09/936,487	FILING DATE 01/25/2002 RULE	CLASS 714	GROUP ART UNIT 2133	ATTORNEY DOCKET NO. 15675P372
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APPLICANTS

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** CONTINUING DATA *****

This application is a 371 of PCT/FR00/00559 03/07/2000

Not Applicable DG

** FOREIGN APPLICATIONS *****

FRANCE 99/02823 03/08/1999

Not Applicable DG

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after allowance	STATE OR COUNTRY FRANCE	SHEETS DRAWING 3	TOTAL CLAIMS 27	INDEPENDENT CLAIMS 3
Verified and Acknowledged Examiner's Signature	<i>DG</i> Initials				

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TITLE

Method for testing integrated circuits with memory element access

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____
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